## N-Channel 200-V (D-S) MOSFET

### **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

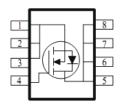
### **Typical Applications:**

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
200	400 @ V <sub>GS</sub> = 10V	3		
	450 @ V <sub>GS</sub> = 4.5V	2.8		







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)							
Parameter				Limit	Units		
Drain-Source Voltage				200	V		
Gate-Source Voltage	-Source Voltage				V		
Continuous Drain Comment <sup>a</sup>		T <sub>A</sub> =25°C		3			
Continuous Drain Current <sup>a</sup>	<u> </u>	T <sub>A</sub> =70°C	l <sub>D</sub>	2.4	Α		
Pulsed Drain Current <sup>b</sup>				20			
Continuous Source Current (Diode Conduction) <sup>a</sup>	Is	6.2	Α				
Davier Dissipation 8		T <sub>A</sub> =25°C	P <sub>D</sub>	5	W		
Power Dissipation <sup>a</sup>			гD	3.2	V V		
Operating Junction and Storage Temperature Range				-55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	$R_{\theta JA}$	25	°C/W			
Maximum Junction-to-Ambient	Steady State	IΛθΊΑ	65	C/VV			

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#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

#### **Electrical Characteristics**

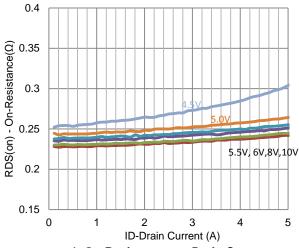
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , ID = 250 uA	1			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±10	uA		
Zero Gate Voltage Drain Current		$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}$			1	1 uA		
	I <sub>DSS</sub>	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA		
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α		
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}$			400	mΩ		
Dialii-Source Off-Resistance	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 2.8 \text{ A}$			450	11177		
Forward Transconductance	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 3.0 \text{ A}$		10		S		
Diode Forward Voltage	$V_{SD}$	$I_S = 3.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.77		V		
		Dynamic						
Total Gate Charge	$Q_g$			7.9		nC		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 4.5 \text{ V}, ID = 2.0 \text{ A}$		3.4				
Gate-Drain Charge	$Q_{gd}$			3.9				
Turn-On Delay Time	t <sub>d(on)</sub>			3.7				
Rise Time	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_L$ = 50 $\Omega$ , $I_D$ = 2.0 A,		8		ns		
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		26				
Fall Time	t <sub>f</sub>			12				
Input Capacitance	C <sub>iss</sub>			807				
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		81		pF		
Reverse Transfer Capacitance	$C_{rss}$		_	38				

#### Notes

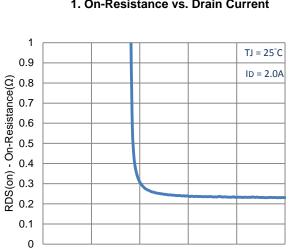
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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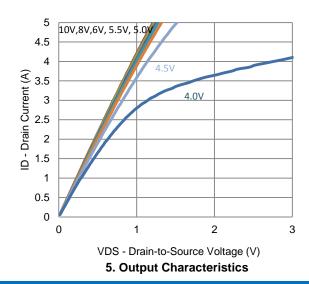
### **Typical Electrical Characteristics**

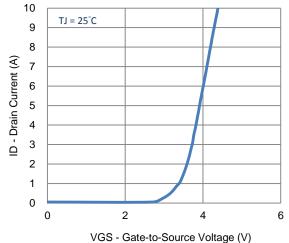


1. On-Resistance vs. Drain Current

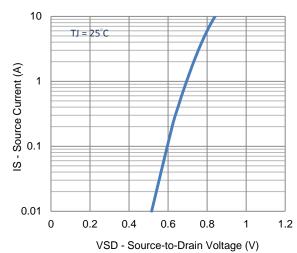


VGS - Gate-to-Source Voltage (V) 3. On-Resistance vs. Gate-to-Source Voltage

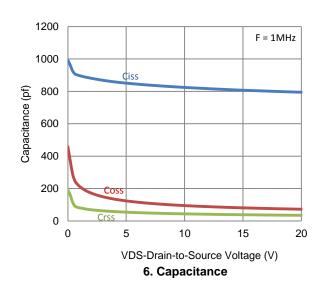




2. Transfer Characteristics

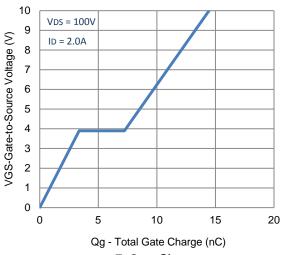


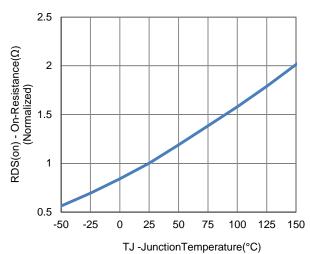
4. Drain-to-Source Forward Voltage



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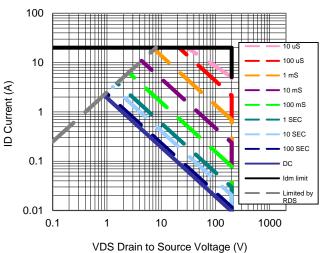
### **Typical Electrical Characteristics**

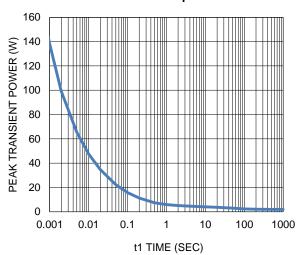






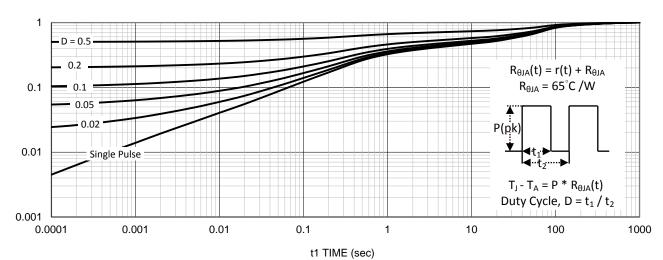






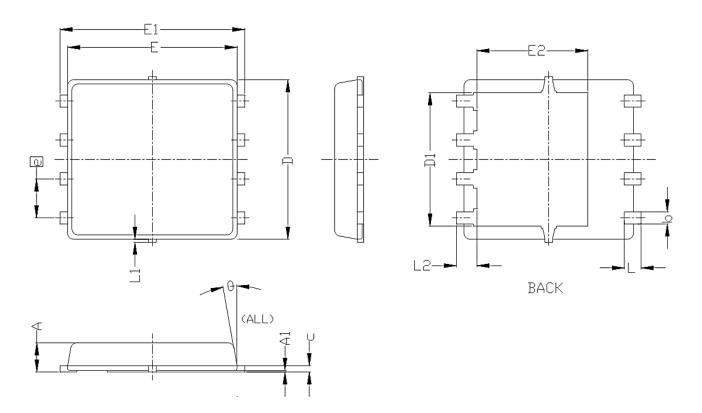
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

# Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.85	0.95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
С	0. 15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 20 BSC			0. 205 BSC			
D1	4. 35 BSC			0. 171 BSC			
Е	5, 55 BSC			0, 219 BSC			
E1	6. 05 BSC			0. 238 BSC			
E2	3. 62 BSC			0. 143 BSC			
e	1. 27 BSC			0. 050 BSC			
L	0.45	0.55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0. 027 REF			
θ	0°		10°	0°		10°	